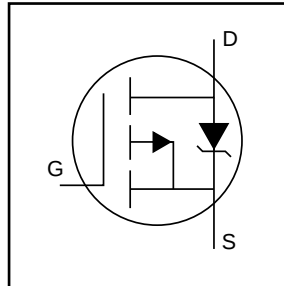


## IRFR/U5305

### HEXFET® Power MOSFET

- Ultra Low On-Resistance
- Surface Mount (IRFR5305)
- Straight Lead (IRFU5305)
- Advanced Process Technology
- Fast Switching
- Fully Avalanche Rated



$$V_{DS} = -55V$$

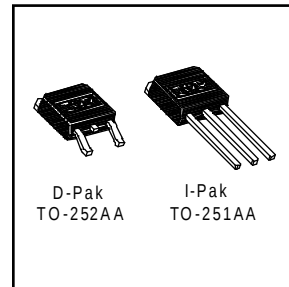
$$R_{DS(on)} = 0.065\Omega$$

$$I_D = -25A$$

### Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The D-Pak is designed for surface mounting using vapor phase, infrared, or wave soldering techniques. The straight lead version (IRFU series) is for through-hole mounting applications. Power dissipation levels up to 1.5 watts are possible in typical surface mount applications.



D-Pak  
TO-252AA

I-Pak  
TO-251AA

### Absolute Maximum Ratings

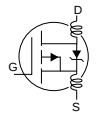
|                             | Parameter                                 | Max.                   | Units |
|-----------------------------|-------------------------------------------|------------------------|-------|
| $I_D$ @ $T_C = 25^\circ C$  | Continuous Drain Current, $V_{GS}$ @ -10V | -25                    | A     |
| $I_D$ @ $T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS}$ @ -10V | -16                    |       |
| $I_{DM}$                    | Pulsed Drain Current ①⑥                   | -110                   |       |
| $P_D$ @ $T_C = 25^\circ C$  | Power Dissipation                         | 69                     | W     |
|                             | Linear Derating Factor                    | 0.56                   | W/°C  |
| $V_{GS}$                    | Gate-to-Source Voltage                    | $\pm 20$               | V     |
| $E_{AS}$                    | Single Pulse Avalanche Energy②⑥           | 280                    | mJ    |
| $I_{AR}$                    | Avalanche Current①⑥                       | -16                    | A     |
| $E_{AR}$                    | Repetitive Avalanche Energy①              | 6.9                    | mJ    |
| $dv/dt$                     | Peak Diode Recovery $dv/dt$ ③⑥            | -5.8                   | V/ns  |
| $T_J$                       | Operating Junction and                    | -55 to +150            | °C    |
| $T_{STG}$                   | Storage Temperature Range                 |                        |       |
|                             | Soldering Temperature, for 10 seconds     | 300 (1.6mm from case ) |       |

### Thermal Resistance

|                 | Parameter                        | Typ. | Max. | Units |
|-----------------|----------------------------------|------|------|-------|
| $R_{\theta JC}$ | Junction-to-Case                 | —    | 1.8  | °C/W  |
| $R_{\theta JA}$ | Junction-to-Ambient (PCB mount)* | —    | 50   |       |
| $R_{\theta JA}$ | Junction-to-Ambient**            | —    | 110  |       |

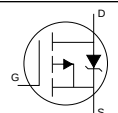
Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)

|                                 | Parameter                            | Min. | Typ.   | Max.  | Units               | Conditions                                                                    |
|---------------------------------|--------------------------------------|------|--------|-------|---------------------|-------------------------------------------------------------------------------|
| $V_{(BR)DSS}$                   | Drain-to-Source Breakdown Voltage    | -55  | —      | —     | V                   | $V_{GS} = 0V$ , $I_D = -250\mu A$                                             |
| $\Delta V_{(BR)DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient  | —    | -0.034 | —     | V/ $^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D = -1mA$                                |
| $R_{DS(on)}$                    | Static Drain-to-Source On-Resistance | —    | —      | 0.065 | $\Omega$            | $V_{GS} = -10V$ , $I_D = -15A$ ④                                              |
| $V_{GS(th)}$                    | Gate Threshold Voltage               | -2.0 | —      | -4.0  | V                   | $V_{DS} = V_{GS}$ , $I_D = -250\mu A$                                         |
| $g_{fs}$                        | Forward Transconductance             | 8.0  | —      | —     | S                   | $V_{DS} = -25V$ , $I_D = -16A$ ⑥                                              |
| $I_{DSS}$                       | Drain-to-Source Leakage Current      | —    | —      | -25   | $\mu A$             | $V_{DS} = -55V$ , $V_{GS} = 0V$                                               |
|                                 |                                      | —    | —      | -250  |                     | $V_{DS} = -44V$ , $V_{GS} = 0V$ , $T_J = 150^\circ\text{C}$                   |
| $I_{GSS}$                       | Gate-to-Source Forward Leakage       | —    | —      | 100   | nA                  | $V_{GS} = 20V$                                                                |
|                                 | Gate-to-Source Reverse Leakage       | —    | —      | -100  |                     | $V_{GS} = -20V$                                                               |
| $Q_g$                           | Total Gate Charge                    | —    | —      | 63    | nC                  | $I_D = -16A$                                                                  |
| $Q_{gs}$                        | Gate-to-Source Charge                | —    | —      | 13    |                     | $V_{DS} = -44V$                                                               |
| $Q_{gd}$                        | Gate-to-Drain ("Miller") Charge      | —    | —      | 29    |                     | $V_{GS} = -10V$ , See Fig. 6 and 13 ④ ⑥                                       |
| $t_{d(on)}$                     | Turn-On Delay Time                   | —    | 14     | —     | ns                  | $V_{DD} = -28V$                                                               |
| $t_r$                           | Rise Time                            | —    | 66     | —     |                     | $I_D = -16A$                                                                  |
| $t_{d(off)}$                    | Turn-Off Delay Time                  | —    | 39     | —     |                     | $R_G = 6.8\Omega$                                                             |
| $t_f$                           | Fall Time                            | —    | 63     | —     |                     | $R_D = 1.6\Omega$ , See Fig. 10 ④ ⑥                                           |
| $L_D$                           | Internal Drain Inductance            | —    | 4.5    | —     | nH                  | Between lead,<br>6mm (0.25in.)<br>from package<br>and center of die contact ⑤ |
| $L_S$                           | Internal Source Inductance           | —    | 7.5    | —     |                     |                                                                               |
| $C_{iss}$                       | Input Capacitance                    | —    | 1200   | —     | pF                  | $V_{GS} = 0V$                                                                 |
| $C_{oss}$                       | Output Capacitance                   | —    | 520    | —     |                     | $V_{DS} = -25V$                                                               |
| $C_{rss}$                       | Reverse Transfer Capacitance         | —    | 250    | —     |                     | $f = 1.0MHz$ , See Fig. 5 ⑥                                                   |



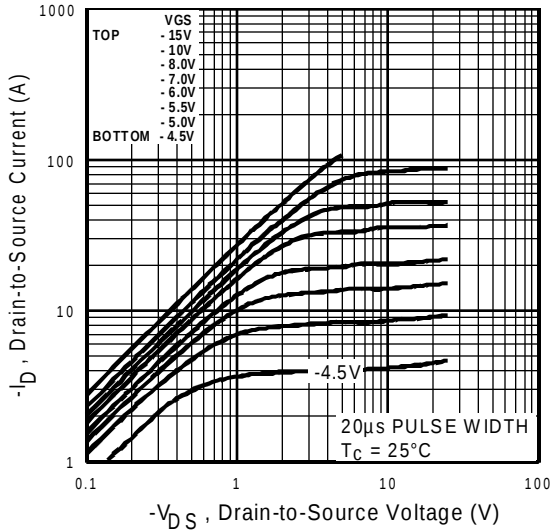
## Source-Drain Ratings and Characteristics

|          | Parameter                                 | Min. | Typ. | Max. | Units | Conditions                                                              |
|----------|-------------------------------------------|------|------|------|-------|-------------------------------------------------------------------------|
| $I_S$    | Continuous Source Current<br>(Body Diode) | —    | —    | -25  | A     | MOSFET symbol<br>showing the<br>integral reverse<br>p-n junction diode. |
| $I_{SM}$ | Pulsed Source Current<br>(Body Diode) ① ⑥ | —    | —    | -110 |       |                                                                         |
| $V_{SD}$ | Diode Forward Voltage                     | —    | —    | -1.3 | V     | $T_J = 25^\circ\text{C}$ , $I_S = -15A$ , $V_{GS} = 0V$ ④               |
| $t_{rr}$ | Reverse Recovery Time                     | —    | 71   | 110  | ns    | $T_J = 25^\circ\text{C}$ , $I_F = -16A$                                 |
| $Q_{rr}$ | Reverse Recovery Charge                   | —    | 170  | 250  | nC    | $di/dt = -100A/\mu s$ ④ ⑥                                               |

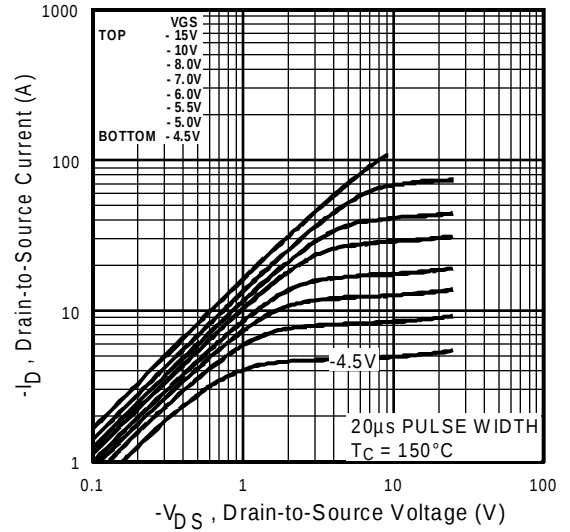


## Notes:

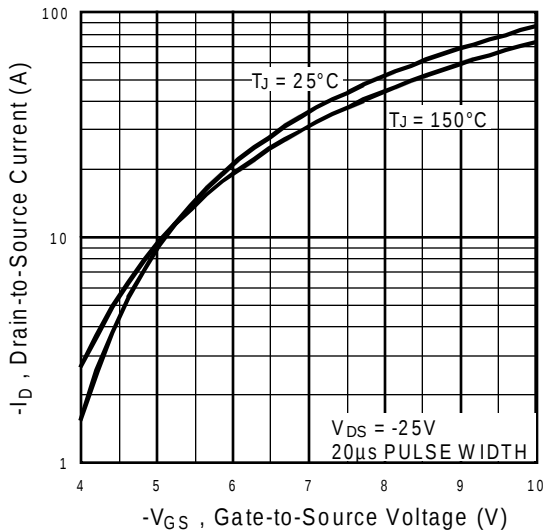
- ① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )
- ②  $V_{DD} = -25V$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 2.3mH$   
 $R_G = 25\Omega$ ,  $I_{AS} = -16A$ . (See Figure 12)
- ③  $I_{SD} \leq -16A$ ,  $di/dt \leq -280A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  
 $T_J \leq 150^\circ\text{C}$
- ④ Pulse width  $\leq 300\mu s$ ; duty cycle  $\leq 2\%$ .
- \* When mounted on 1" square PCB (FR-4 or G-10 Material ) .  
For recommended footprint and soldering techniques refer to application note #AN-994
- \*\* Uses typical socket mount
- ⑤ This is applied for I-PAK,  $L_S$  of D-PAK is measured between lead and center of die contact
- ⑥ Uses IRF5305 data and test conditions.



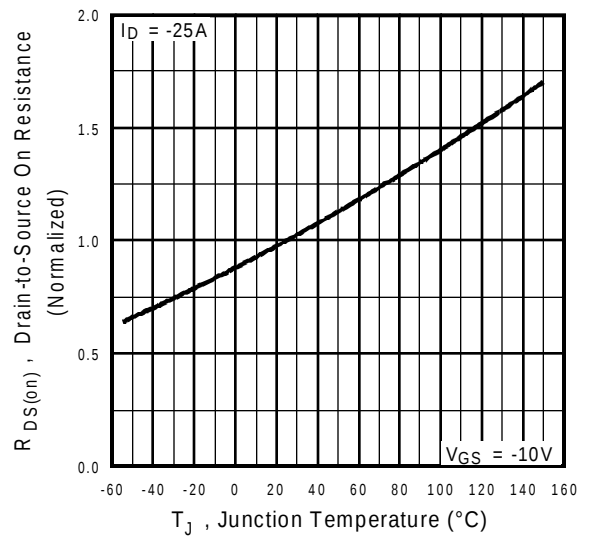
**Fig 1.** Typical Output Characteristics,  
 $T_J = 25^\circ\text{C}$



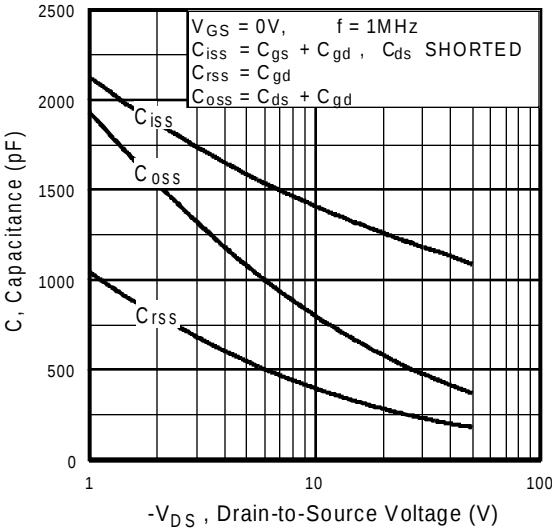
**Fig 2.** Typical Output Characteristics,  
 $T_J = 150^\circ\text{C}$



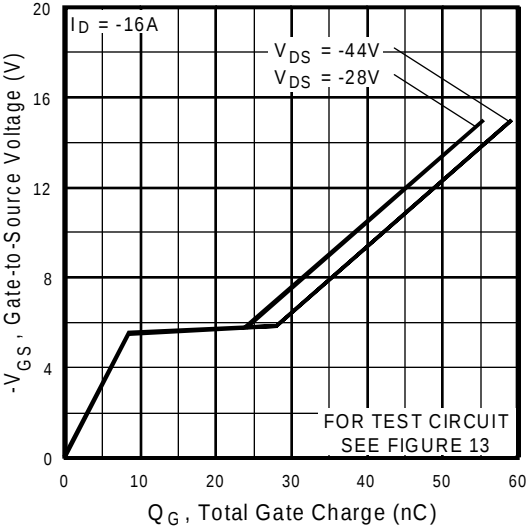
**Fig 3.** Typical Transfer Characteristics



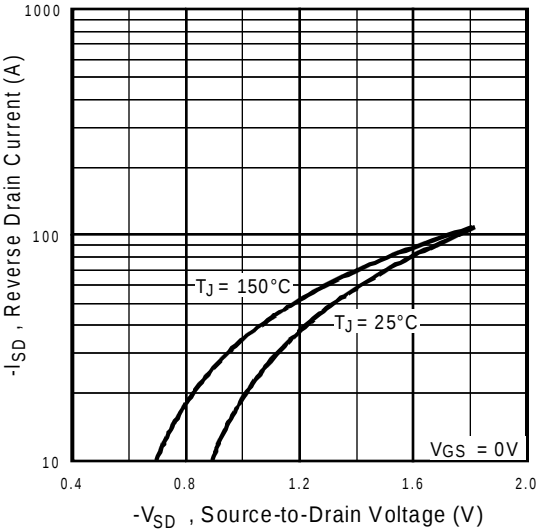
**Fig 4.** Normalized On-Resistance  
Vs. Temperature



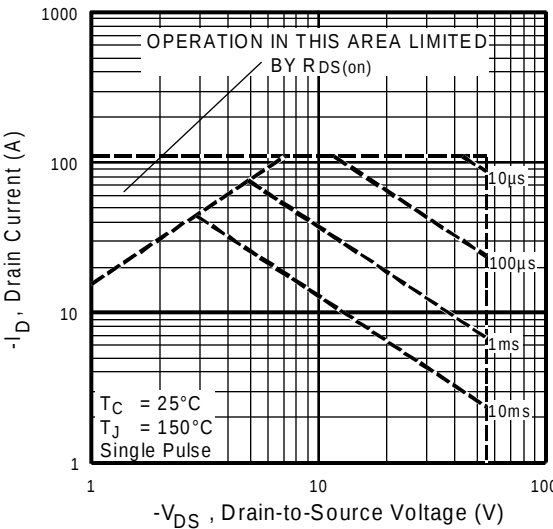
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



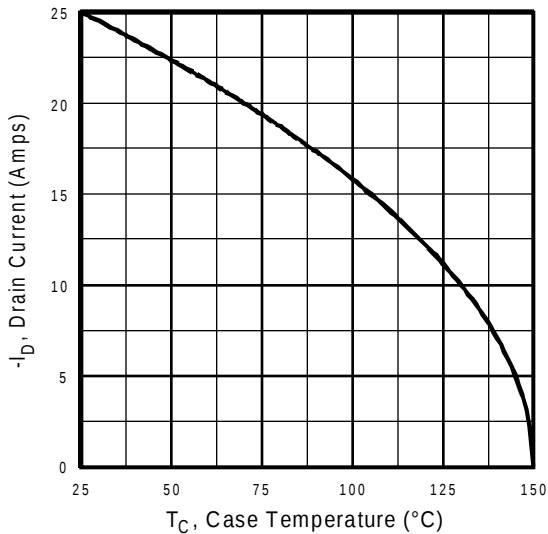
**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



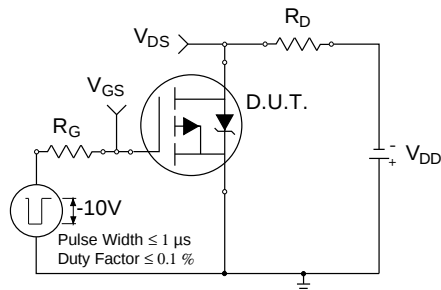
**Fig 7.** Typical Source-Drain Diode Forward Voltage



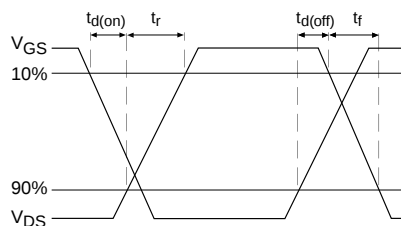
**Fig 8.** Maximum Safe Operating Area



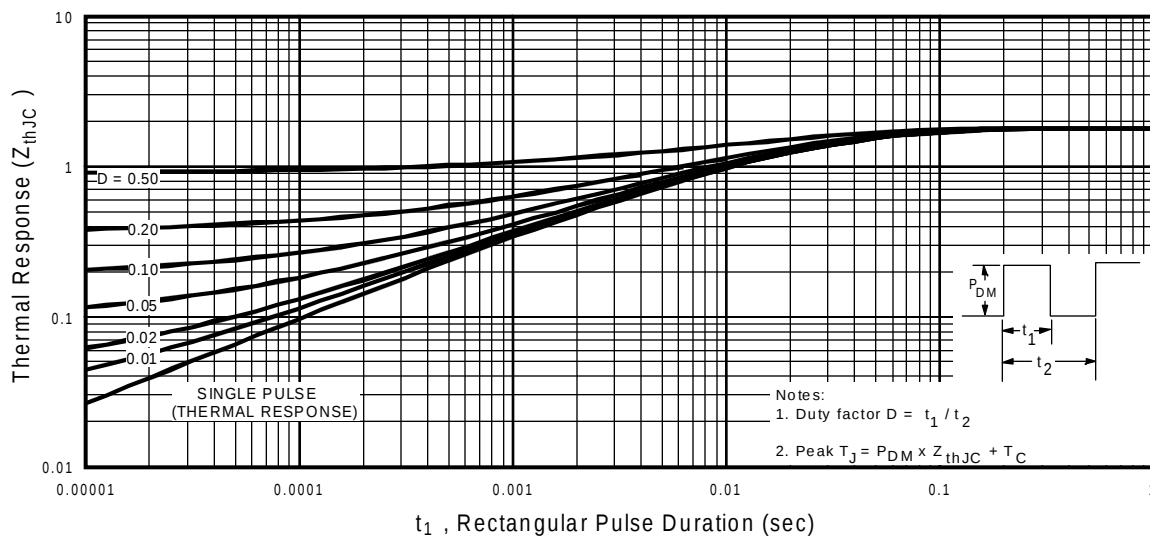
**Fig 9.** Maximum Drain Current Vs. Case Temperature



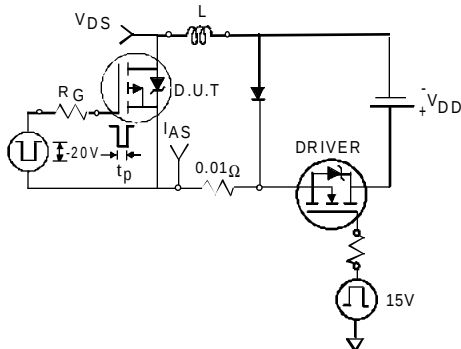
**Fig 10a.** Switching Time Test Circuit



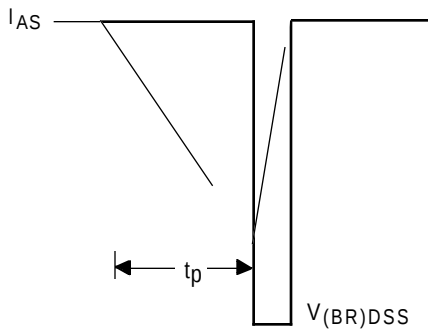
**Fig 10b.** Switching Time Waveforms



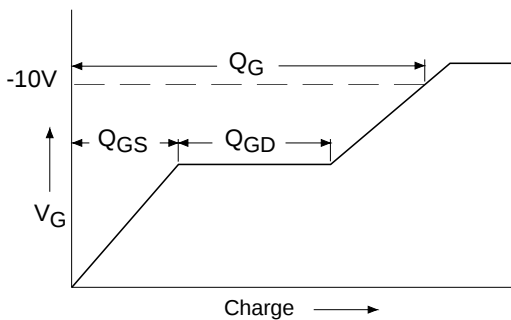
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case



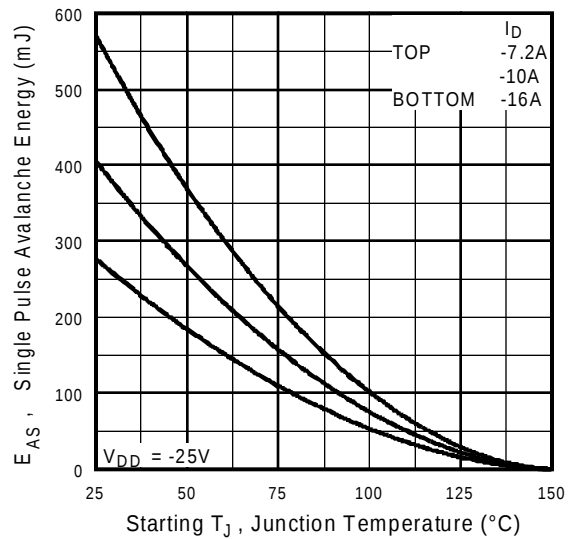
**Fig 12a.** Unclamped Inductive Test Circuit



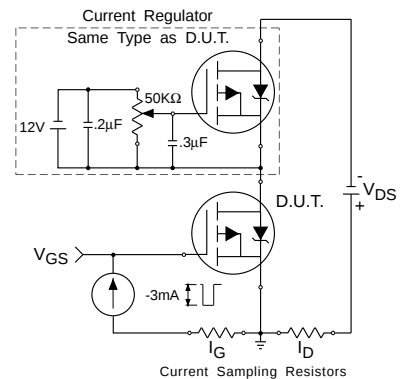
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 13a.** Basic Gate Charge Waveform

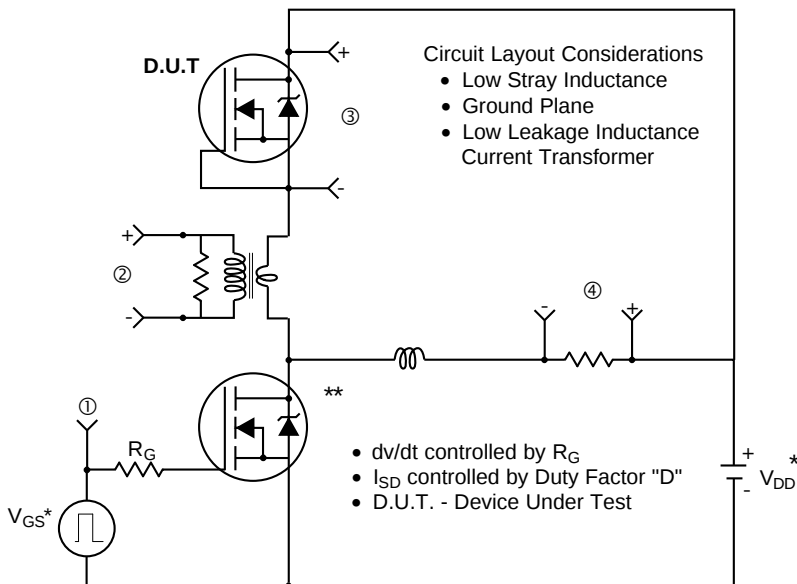


**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



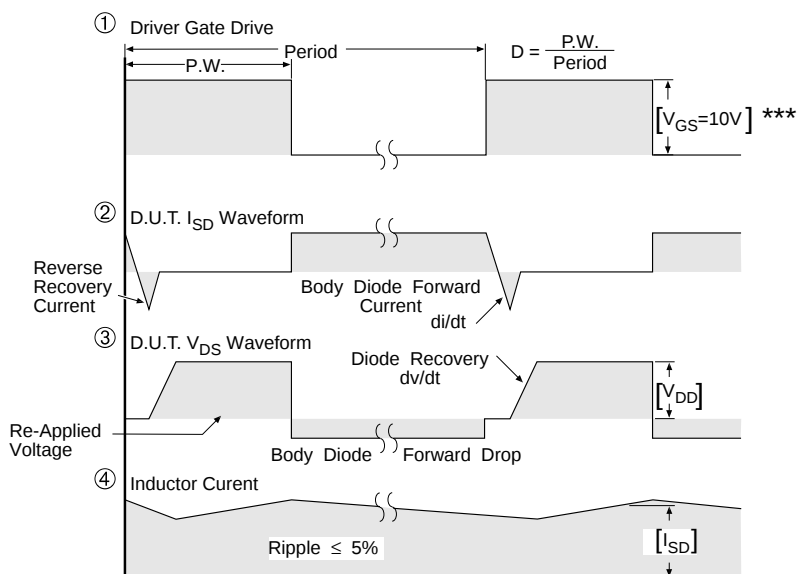
**Fig 13b.** Gate Charge Test Circuit

## Peak Diode Recovery dv/dt Test Circuit



\* Reverse Polarity for P-Channel

\*\* Use P-Channel Driver for P-Channel Measurements

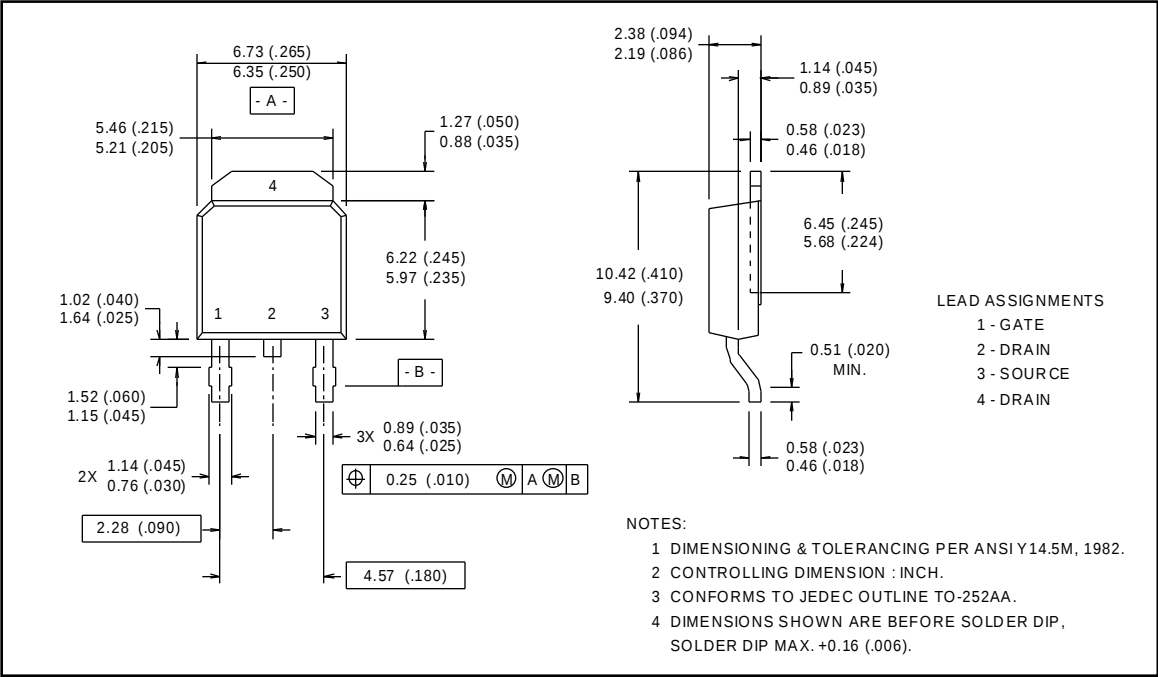


\*\*\*  $V_{GS} = 5.0V$  for Logic Level and 3V Drive Devices

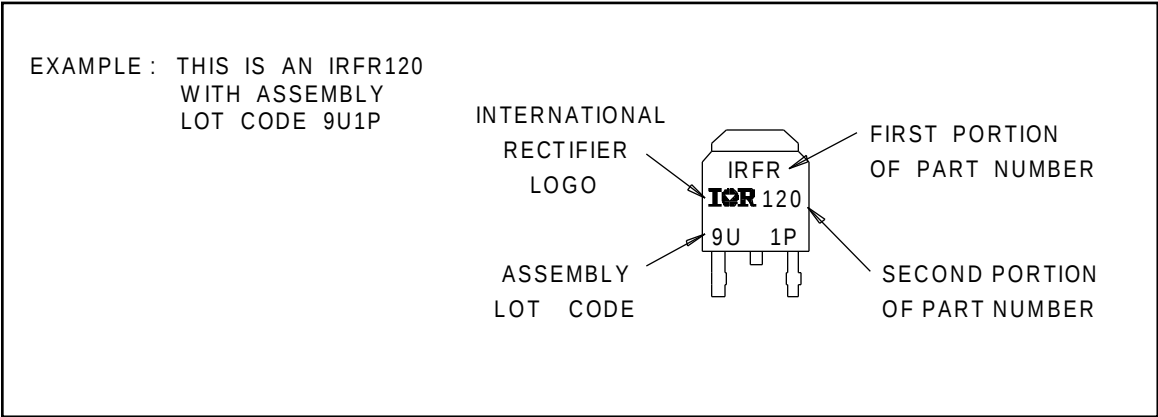
**Fig 14.** For P-Channel HEXFETS

Package Outline — TO-252AA (D-Pak)

Dimensions are shown in millimeters (inches)

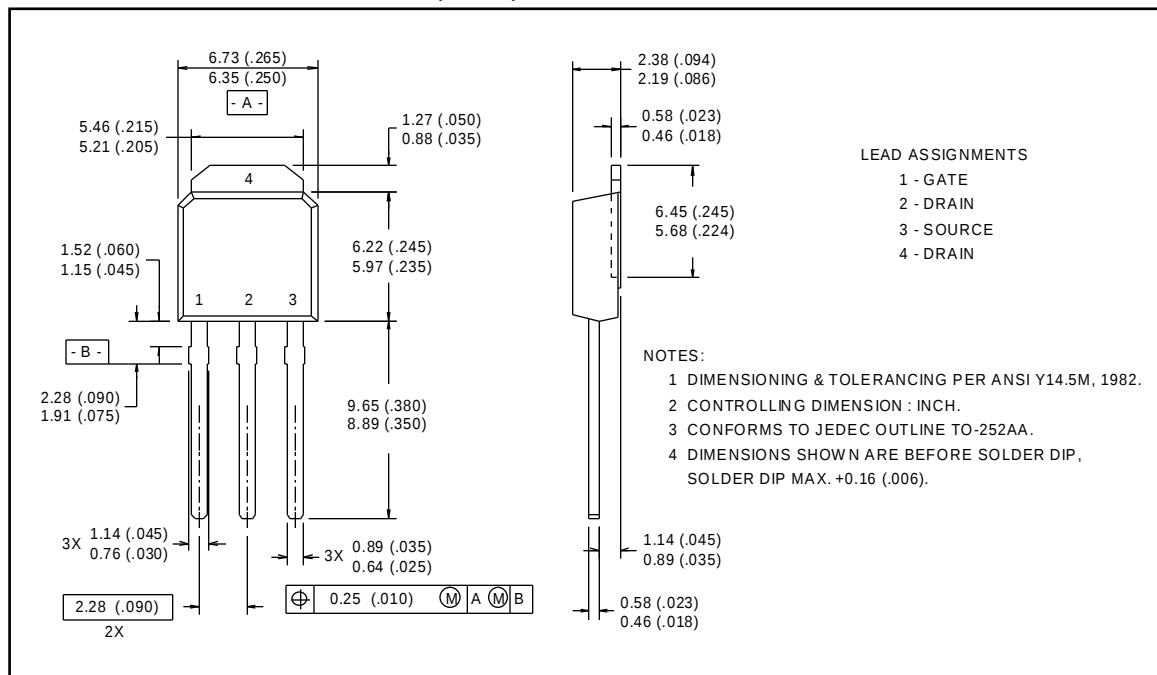


Part Marking Information



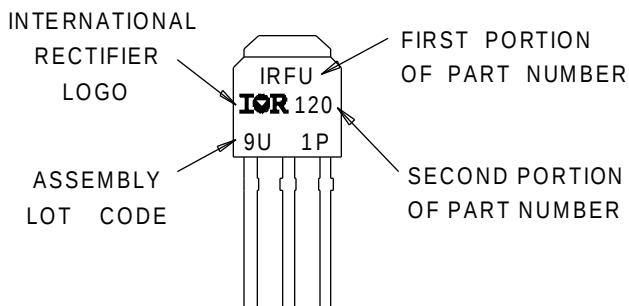
## Package Outline — TO-254AA (I-Pak)

Dimensions are shown in millimeters (inches)

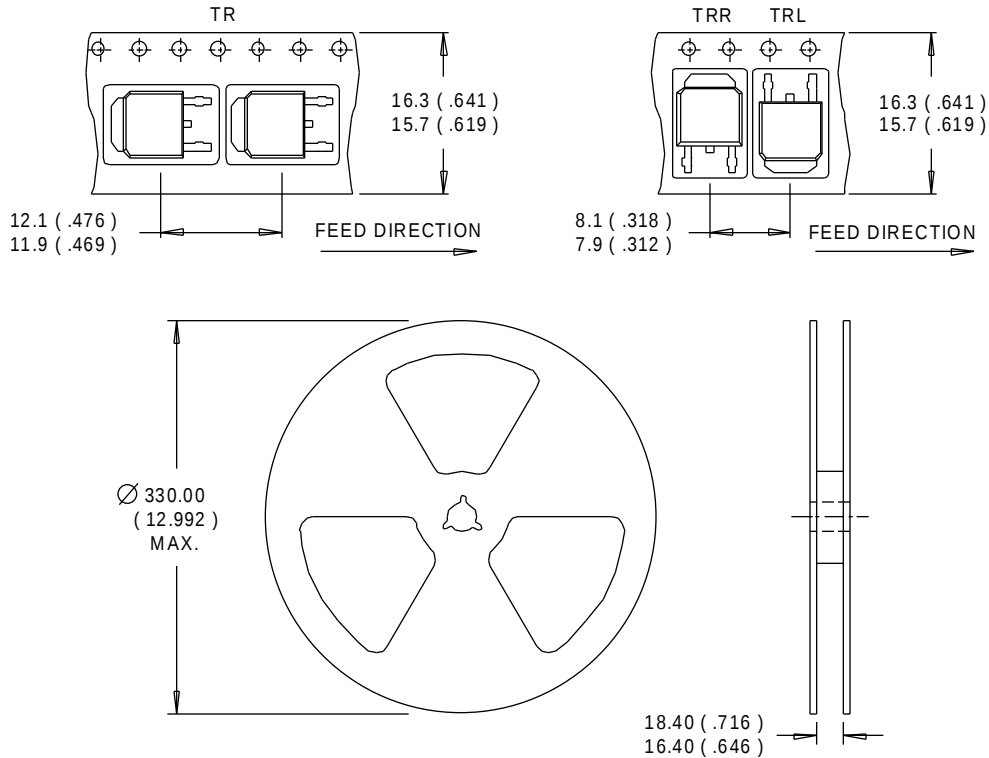


## Part Marking Information

EXAMPLE: THIS IS AN IRFU120  
WITH ASSEMBLY  
LOT CODE 9U1P



Tape & Reel — TO-252AA (D-Pak)



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS ( INCHES ).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.

International  
**IOR** Rectifier

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**IR CANADA:** 7321 Victoria Park Ave., Suite 201, Markham, Ontario L3R 2Z8, Tel: (905) 475 1897

**IR GERMANY:** Saalburgstrasse 157, 61350 Bad Homburg Tel: ++ 49 6172 96590

**IR ITALY:** Via Liguria 49, 10071 Borgaro, Torino Tel: ++ 39 11 451 0111

**IR FAR EAST:** K&H Bldg., 2F, 3-30-4 Nishi-Ikeburo 3-Chome, Toshima-Ki, Tokyo Japan 171 Tel: 81 3 3983 0086

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